NOTES:
1. PWDN should be connected to ground outside of module if unused.
2. RESETB should be connected to DOVDD outside of module if unused.
3. FREX should be connected to DGND outside of module if unused.
4. AVDD is 2.6-3.1V of sensor analog power (clean). During OTP programming, an AVDD voltage range of 2.5V+10% is required. OTP read may use normal AVDD voltage range.
5. DOVDD is 1.7-3.1V of sensor digital IP power (clean). 1.8V is recommended.
6. DVDD is 1.5V of sensor core power (clean). The sensor internal regulator (no external 1.5V DVDD is needed) is recommended for 1.8V DOVDD. External 1.5V DVDD is recommended for 2.8V DOVDD.
7. Sensor AGND and DGND should be separated and connected to a single point outside PCB (Do not be connected inside module).
8. DGND and EGND should be two separated nets, and only connected at a single point inside the module.
9. Capacitors should be close to the related sensor pins.
10. If more space available, use capacitor of 1UF-0402 between DVDD and DGND.
11. D9:0 (D9:MSB, D0:LSB) is sensor RGB RAW 10 bit output. D9:2 (D9:MSB, D2:LSB) is 8 bit output.
12. EVDD/EGND are power/ground for MIPI core. MCP and MCN are MIPI clock lane positive and negative output. MDP0 and MDN0 are MIPI 1st data lane positive and negative output. MDPI and MDN1 are MIPI 2nd data lane positive and negative output.

13. The traces of MCP, MCN, MDP0, MDN0, MDPI and MDN1 should have the same or similar length. The impedance transmission lines should be controlled at 100 Ohm.
14. The traces from AF_VCC to L1+ and L1- to pin VCMSINK, and the trace of AF_AGND need to be paid extra attention because of high electrical current.
15. The VCM driver built in the sensor could be controlled by the micro-controller inside the sensor.